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9/29/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Chen, et al.

Serial No.: 09/748,256 Group Art Unit: 2811

Filed: December 27, 2000 Examiner: Ori Nadav

For: METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE  
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-  
INSULATOR (SOI) SUBSTRATE

Honorable Commissioner of Patents  
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

In response to the Office Action dated May 20, 2003, please amend the above-  
identified application as follows:

INTRODUCTORY COMMENTS

Claims 29-39 and 41-57 are all the claims presently pending in the application.  
Claims 29-39 and 41-54 has been amended to more clearly define the invention. Claims are  
29, 41 and 45-46 independent.

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